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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Date: July 29, 2002

D. Singh et al.

Group Art Unit:

Serial No.: To be assigned

Examiner: To be assigned

Filed: Herewith

Docket No. YOR920010768US1

For: Enhanced T-gate Structure for Modulation Doped Field Effect Transistors

Assistant Commissioner for Patents Washington, D.C. 20231

APPOINTMENT OF ASSOCIATE POWER OF ATTORNEY

Sir:

Please recognize George Sai-Halasz, Reg. No. 45,430, of 145 Fernwood Dr., E. Greenwich, RI 02818, T: 401-885-8032 as associate attorney, with full power to prosecute, inspect, and transact all business concerning the above-identified application, including the filing of amendments, continuation applications, divisional applications and continuation-in-part applications.

Please send all official correspondence to George Sai-Halasz, Reg. No. 45,430, to the above noted address.

Respectfully submitted,

Robert M. Trepp

Reg. No. 25,933 (914) 945-3147

IBM CORPORATION
Intellectual Property Law Dept.
P.O. Box 218
Yorktown Heights, New York 10598

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

ENHANCED T-GATE STRUCTURE FOR MODULATION DOPED FIELD EFFECT TRANSISTORS

the specification of which (check of	ne)			
X is attached hereto.				
was filed on	as			
Application Serial No				
and was amended on	(if applicable)			
I hereby state that I have reviewed a above.	nd understand the contents of the abo	ve identified specification, inc	cluding the claims, as amended by any amendment referred to	
I acknowledge the duty to disclose i Section 1.56.	nformation which is material to the pa	atentability of this application	in accordance with Title 37, Code of Federal Regulations,	
•	fits under Title 35, United States Code gn application for patent or inventor's	e, Section 119 of any foreign a certificate having a filing date	pplication(s) for patent or inventor's certificate listed below and before that of the application on which priority is claimed:	
Prior Foreign Application(s)			Priority Claimed	
manner provided by the first paragra application as defined in Title 37, Co international filing date of this appli	ph of Title 35, United States Code, Section 1	c claims of the application is i	Yes No (Day/Month/Year Filed) United States Provisional Application(s) and Nonprovisional not disclosed in the prior United States application in the duty to disclose information material to the patentability of this are filing date of the prior application and the national or PCT	
(Application Serial No.)		(Filing Date)	(Status) (patented, pending, abandoned)	
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that willful false statements may jeopardize the validity of the application or any patent issued thereon.				
Patent and Trademark Office connect Number and filing date when receive	ned inventor I hereby appoint the follo ted therewith (list name and registration of from the United States Patent and T	owing attorney(s) and/or agent on number). The Attorney of rademark Office.	(s) to prosecute this application and transact all business in the Record herein is hereby authorized to insert the Application	
Cameron (Reg. 31,596), Louis P. Morris (Reg. 32,053), Louis I. F.	Herzhera (Dea 41 500) Maria	Underweiser (Reg. 46,13	(Reg. 26,914), John E. Hoel (Reg. 26,279), Joseph C. 010), Marc A. Erlich (Reg. 39,966), Douglas W. 4), Stephen C. Kaufman (Reg. 29,551), Daniel P. H. Zarick (Reg. 43,303), Robert P. Tassinari, Jr. (Reg. M. Farrell (Reg. No. 37,321).	
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DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

ENHANCED T-GATE STRUCTURE FOR MODULATION DOPED FIELD EFFECT TRANSISTORS

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